

APR 2 5 2005 1



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/822,506

Filed:

April 12, 2004

Inventor(s):

John V. Sell

Title:

**CPU AND GRAPHICS** 

UNIT WITH SHARED

**CACHE** 

Examiner:

Singh, Dalip K.

Group/Art Unit:

2676

Atty. Dkt. No:

5500-98000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Rory D. Rankin
Printed Name

April 22, 2005
Signature Date

## RESPONSE TO OFFICE ACTION OF MARCH 24, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of March 24, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.